

V.H.F. POWER TRANSISTOR

N-P-N planar epitaxial transistor intended for use in class-A, B and C operated mobile, industrial and military transmitters with a supply voltage of 28 V. The transistor is resistance stabilized. Every transistor is tested under severe load mismatch conditions. It has a plastic encapsulated stripline package. All leads are isolated from the stud.

QUICK REFERENCE DATA

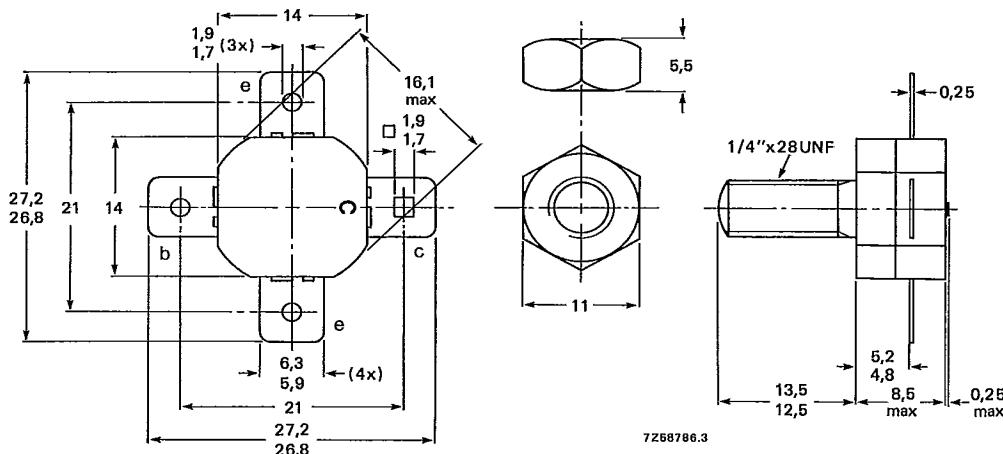
R.F. performance up to $T_{mb} = 25^{\circ}\text{C}$ in an unneutralized common-emitter class-B circuit

mode of operation	V_{CE} V	f MHz	P_S W	P_L W	I_C A	G_p dB	η %	\overline{z}_i Ω	\overline{Y}_L mS
c.w.	28	175	< 10	50	< 2,75	> 7	> 65	$0,8 + j1,45$	$125 - j66$

MECHANICAL DATA

Dimensions in mm

Fig. 1 SOT-55.



Torque on nut: min. 2,3 Nm
(23 kg cm)
max. 2,7 Nm
(27 kg cm)

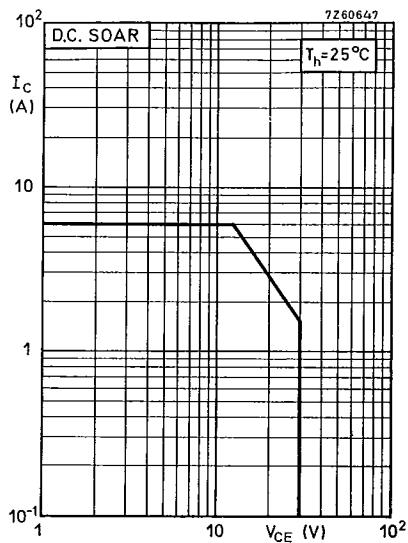
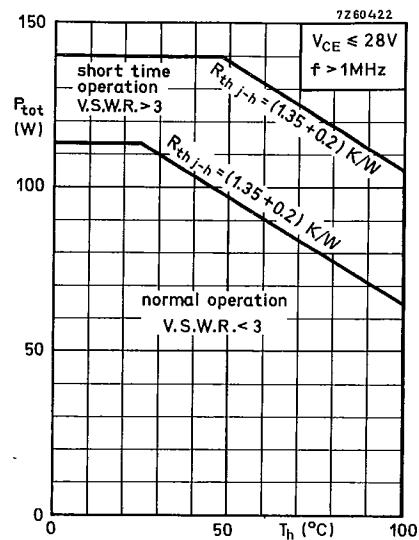
Diameter of clearance hole in heatsink: max. 6,4 mm.
Mounting hole to have no burrs at either end.
De-burring must leave surface flat; do not chamfer or
countersink either end of hole.

When locking is required an adhesive is preferred instead of a lock washer.

PRODUCT SAFETY This device incorporates beryllium oxide, the dust of which is toxic. The device is entirely safe provided that the BeO disc is not damaged.

RATINGS Limiting values in accordance with the Absolute Maximum System (IEC 134)

Collector-base voltage (open emitter) peak value	V _{CBOM}	max.	65	V
Collector-emitter voltage (open base)	V _{CCEO}	max.	36	V
Emitter-base voltage (open collector)	V _{EBO}	max.	4	V
Collector current (average)	I _{C(AV)}	max.	6	A
Collector current (peak value) f > 1 MHz	I _{CM}	max.	12	A
Total power dissipation up to T _{mb} = 25 °C f > 1 MHz	P _{tot}	max.	130	W



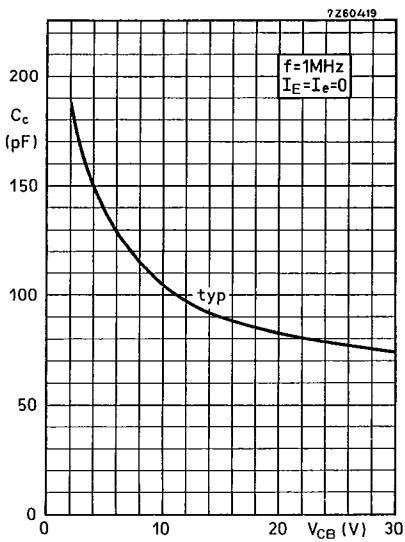
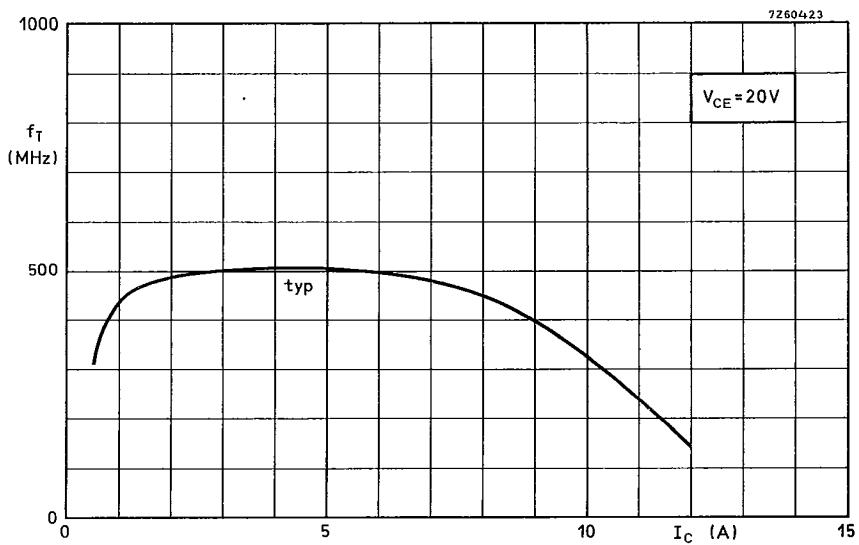
Storage temperature
Operating junction temperature

T_{stg} -65 to +200 °C
T_j max. 200 °C

THERMAL RESISTANCE

From junction to mounting base
From mounting base to heatsink

R_{th j-mb} = 1.35 K/W
R_{th mb-h} = 0.2 K/W



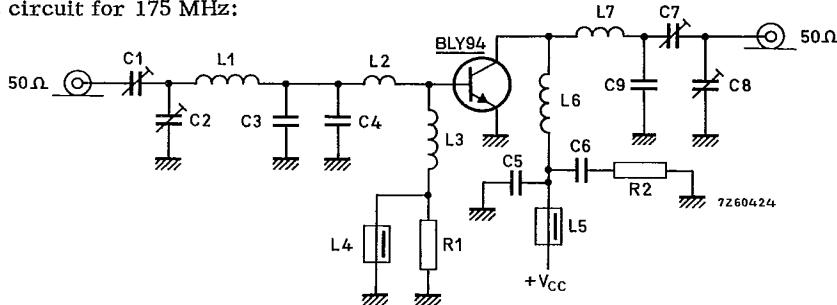
APPLICATION INFORMATION

R. F. performance in c. w. operation (unneutralised common-emitter class B circuit)

 $f = 175 \text{ MHz}$; T_{mb} up to 25°C

$V_{CC} (\text{V})$	$P_S (\text{W})$	$P_L (\text{W})$	$I_G (\text{A})$	$G_p (\text{dB})$	$\eta (\%)$	$\overline{z_1} (\Omega)$	$\overline{Y_L} (\text{mS})$
28	< 10	50	< 2.75	> 7	> 65	$0.8 + j1.45$	$125 - j66$

Test circuit for 175 MHz:



List of components:

C1 = 2 to 20 pF film dielectric trimmer (code number 2222 809 07004)

C2 = 4 to 40 pF film dielectric trimmer (code number 2222 809 07008)

C3 = C4 = 56 pF ceramic

C5 = 100 pF ceramic

C6 = 100 nF polyester

C7 = 4 to 60 pF film dielectric trimmer (code number 2222 809 07011)

C8 = 4 to 100 pF film dielectric trimmer (code number 2222 809 07015)

C9 = 6.8 pF ceramic

L1 = 36nH; 2 turns enamelled Cu wire (1.5 mm); int. diam. 7mm; length 5 mm; lead length 2x 5 mm

L2 = formed by the metallization on the p.c. board; see component lay-out

L3 = 100 nH; 7 turns closely wound enamelled Cu wire (0.5 mm); int. diam 3 mm; lead length 2x 5 mm

L4 = L5 = ferroxcube choke (code number 4 312 020 36640)

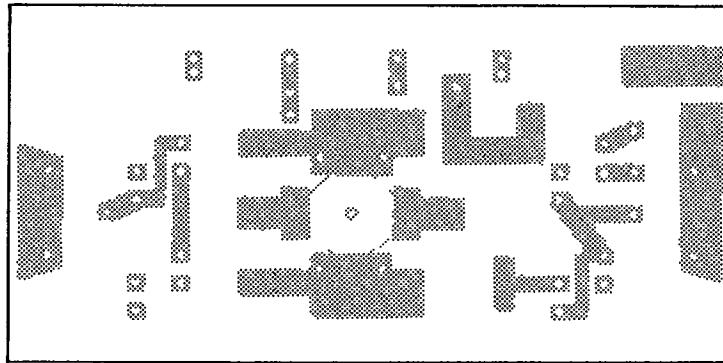
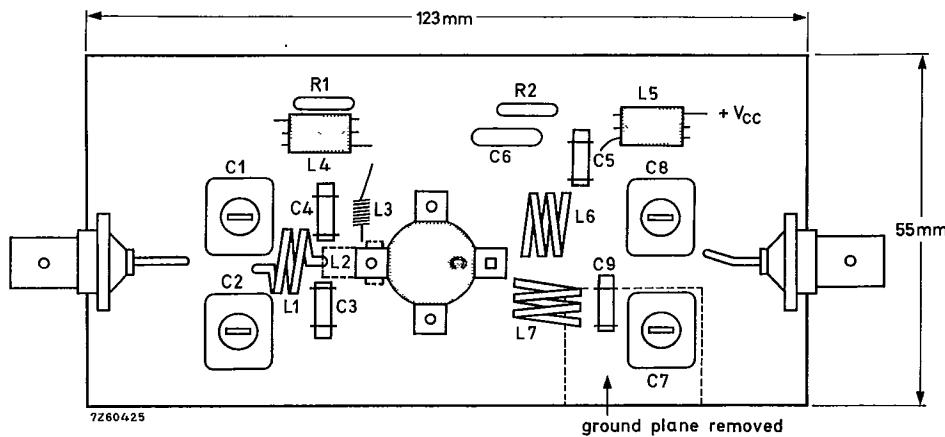
L6 = 53 nH; 2 turns enamelled Cu wire (1.5 mm); int. diam. 10 mm; length 5.2 mm; lead length 2x 5 mm

L7 = 46 nH; 2 turns enamelled Cu wire (1.5 mm); int. diam. 9 mm; length 5.4 mm; lead length 2x 5 mm

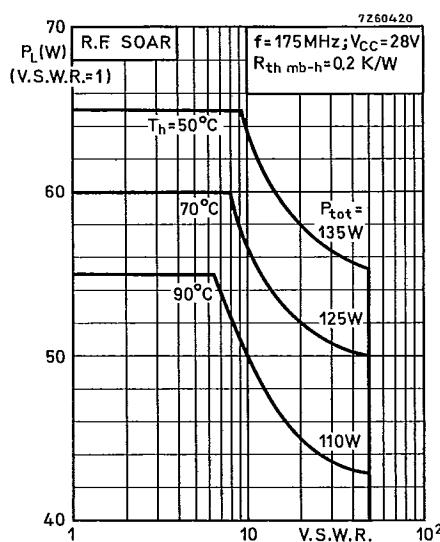
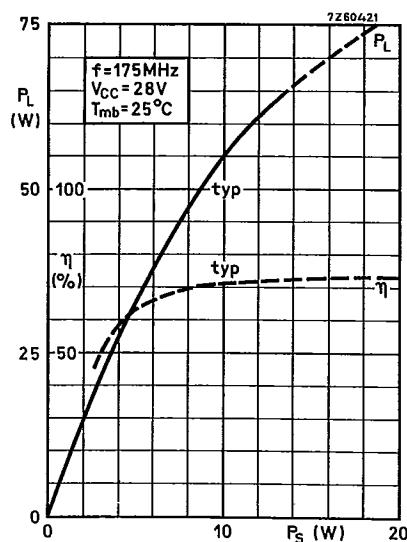
R1 = R2 = 10 Ω carbon

APPLICATION INFORMATION (continued)

Component lay-out and printed circuit board for 175 MHz test circuit.



The circuit and the components are situated on one side of the epoxy fibre-glass board, the other side being fully metallised to serve as earth. Earth connections are made by means of hollow rivets.



For high voltage operation, a stabilized power supply is generally used.
The graph shows the allowable output power under nominal conditions as a function of the V.S.W.R., with heat-sink temperature as parameter.

OPERATING NOTE Below 50 MHz a base-emitter resistor of $10\ \Omega$ is recommended to avoid oscillation. This resistor must be effective for both d.c. and r.f.

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